

## Pulse-Width Modulator Operates at Various Levels of Frequency and Power

Build a general-purpose pulse-width modulator using three op amps from a quad-op-amp device.

The many applications for pulse-width modulation (PWM) include voltage regulation, power-level control, and fan-speed control. A PWM circuit for such applications (Figure 1) can be implemented with three op amps from a single quad-op-amp chip. The use of op amps allows a wide variety of applications. Low-power op amps can be used in a low-power system, for example, and high-frequency op amps can be used for a high-frequency PWM. The Figure 1 circuit also generates a triangular wave.

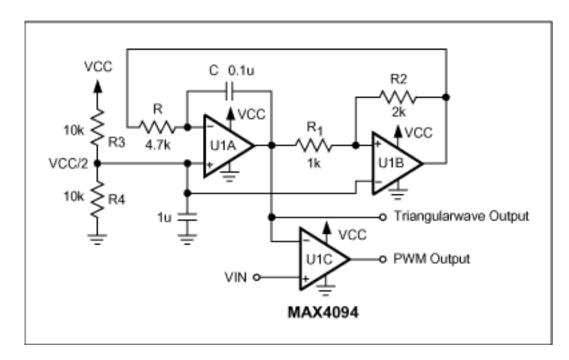


Figure 1. This 3-op-amp circuit produces a triangular wave and a variable-pulsewidth output.

The circuit consists of a triangle-wave generator (U1A and U1B) and a comparator (U1C). U1A is configured as an integrator (or de-integrator), and U1B as a comparator with hysteresis. At power-up, the comparator's output voltage is assumed to be zero.

U1A's non-inverting input is biased at VCC/2. A virtual connection between the inverting and non-inverting inputs allows a constant current through R equal to I = VCC/2R, which charges the capacitor C. Thus, the U1A integrator output increases linearly with time. When it reaches

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0.75VCC, the comparator output (U1B) changes to its maximum output voltage (VCC). At that point the integrator begins to de-integrate, causing the output voltage to decrease linearly. When it reaches 0.25VCC the comparator output voltage changes to zero, and the cycle repeats. Thus, the integrator output is a triangular wave that swings between the levels of ½VCC and ¾VCC.

U1C compares the triangular wave against the dc level VIN. Its output is a square wave, with a duty cycle that varies from 0% to 100% as VIN varies from  $\frac{1}{4}$ /CC to  $\frac{3}{4}$  VCC (Figure 2). Frequency is determined by R, C, R<sub>1</sub>, and R<sub>2</sub>:

$$f = \frac{R_2}{4RCR_1}$$
, where  $R2 > R1$ .

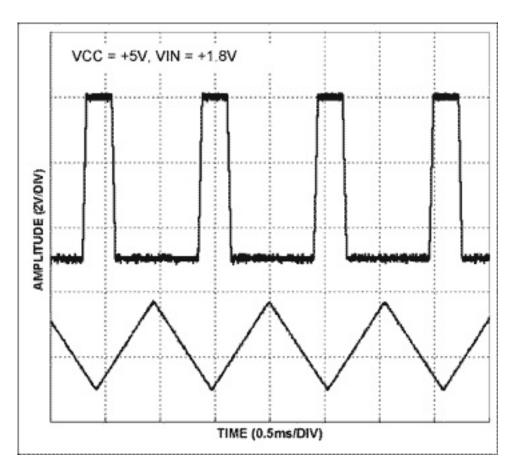


Figure 2. The Figure 1 circuit produces these PWM and triangular-wave outputs.

The ratio of  $R_2$  and  $R_1$  affects the operating frequency and the amplitude of the triangular wave. Given that  $V_{TH}$  is the triangular wave's maximum voltage and  $V_{TL}$  is its minimum voltage, the amplitude swing is:

$$\begin{split} V_{TH} &= \frac{VCC(R_1+R_2)}{2R_2} \mbox{ and } V_{TL} = \frac{VCC(R_2-R_1)}{2R_2} \mbox{, where } R2 > R1. \end{split}$$
 Therefore,  $V_{TH} - V_{TL} = \frac{R_1}{R_2} \mbox{ VCC} \ (R2 > R1). \end{split}$ 

The triangular wave's peak-to-peak voltage (the difference in its maximum and minimum voltages) is centered at the VCC/2 bias voltage generated by  $R_3$  and  $R_4$ . The circuit configuration shown allows the PWM to operate on a single supply. Use micropower op amps and larger resistors (R and  $R_1 - R_4$ ) for low-power applications, and high-frequency op amps for higher-frequency applications. (The quad op amp shown comes in a single package.)

## **More Information**

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